

Fig. 1

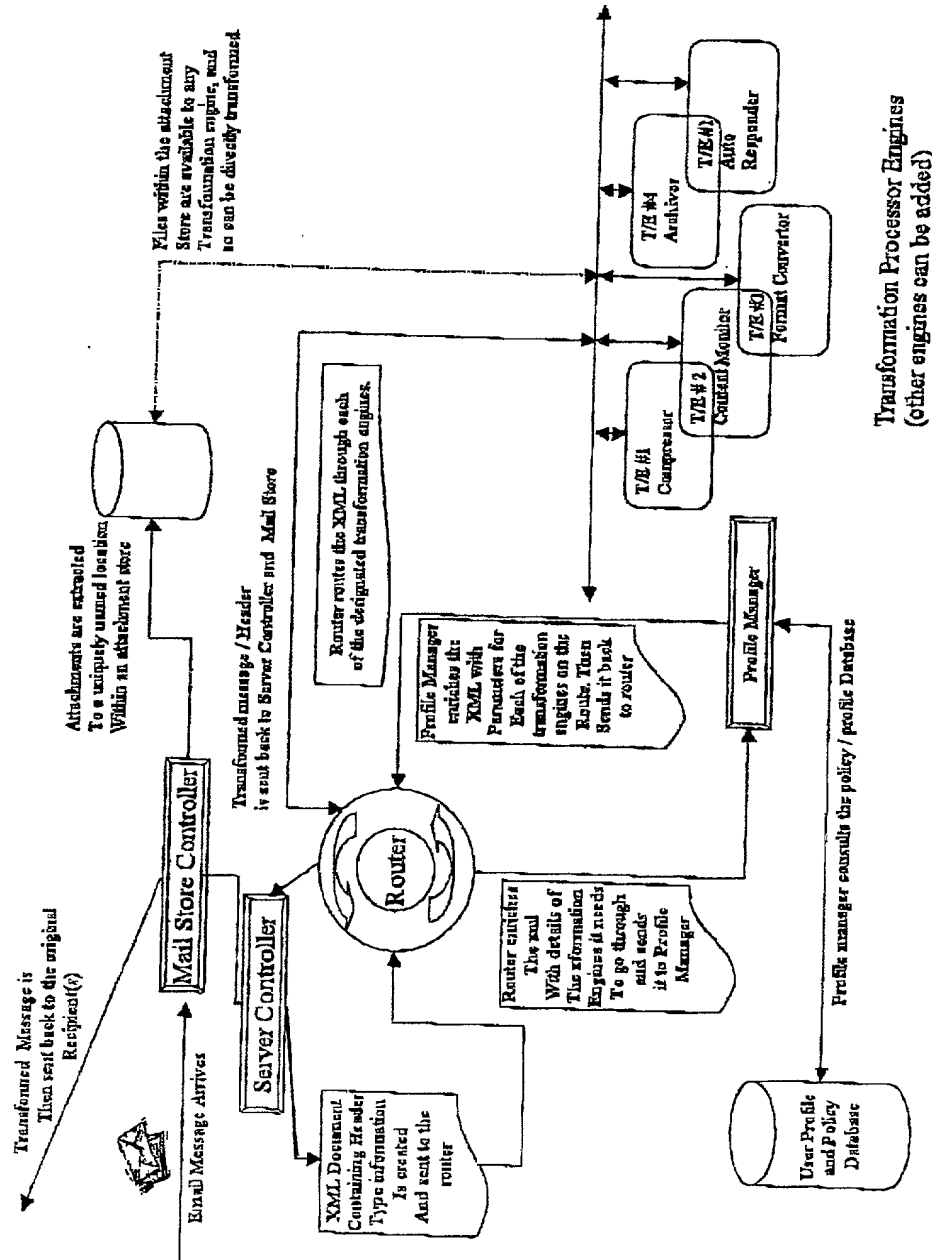


Fig. 2

Transformation Processor Engines
(other engines can be added)

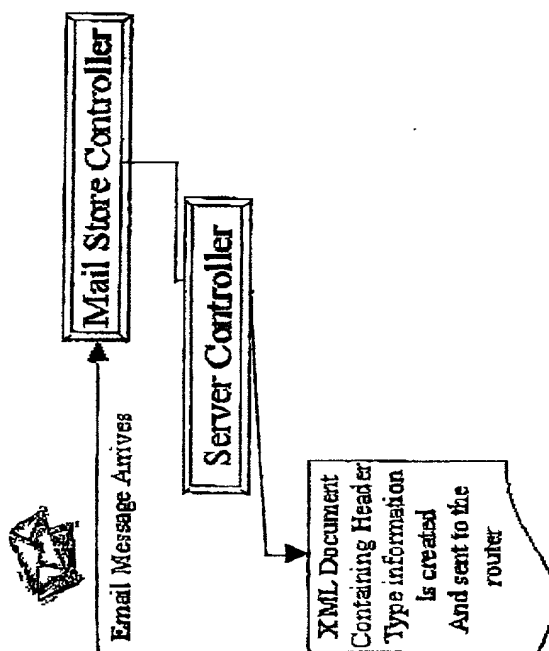


Fig. 3

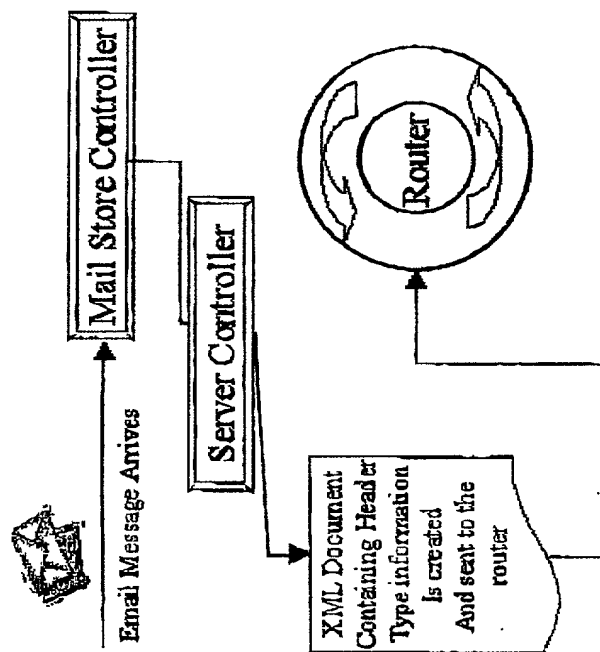


Fig. 4

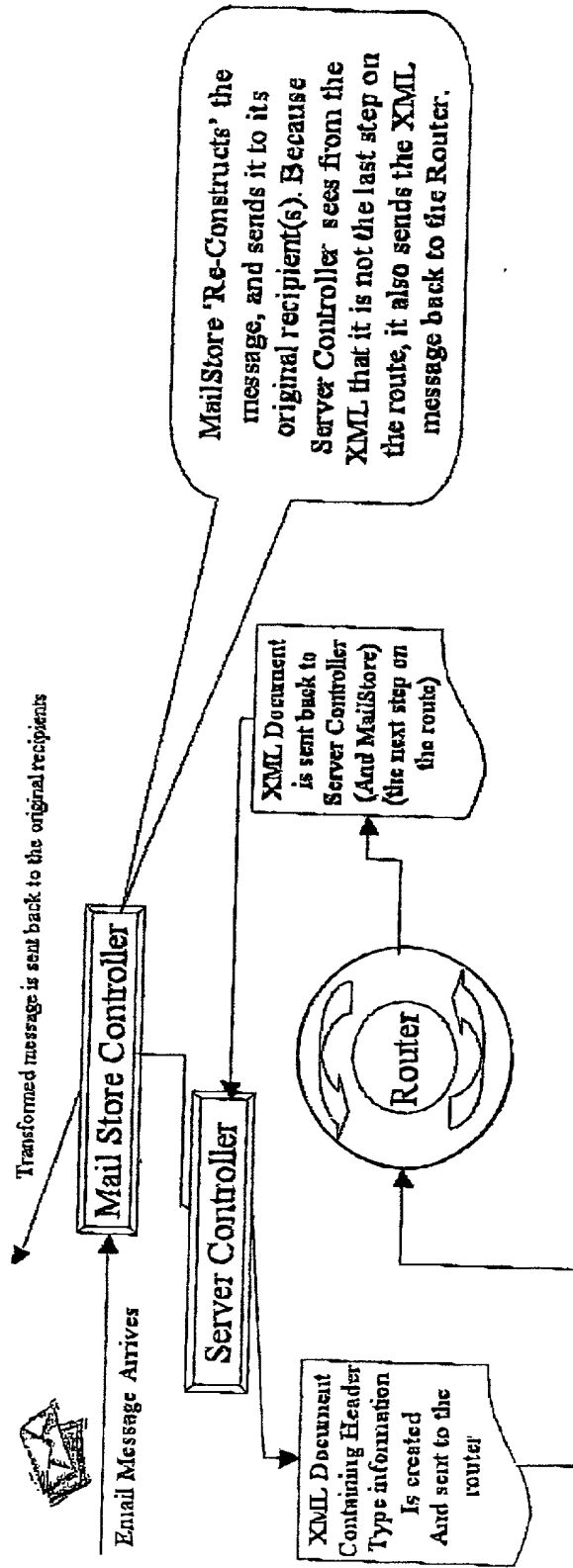


Fig. 5

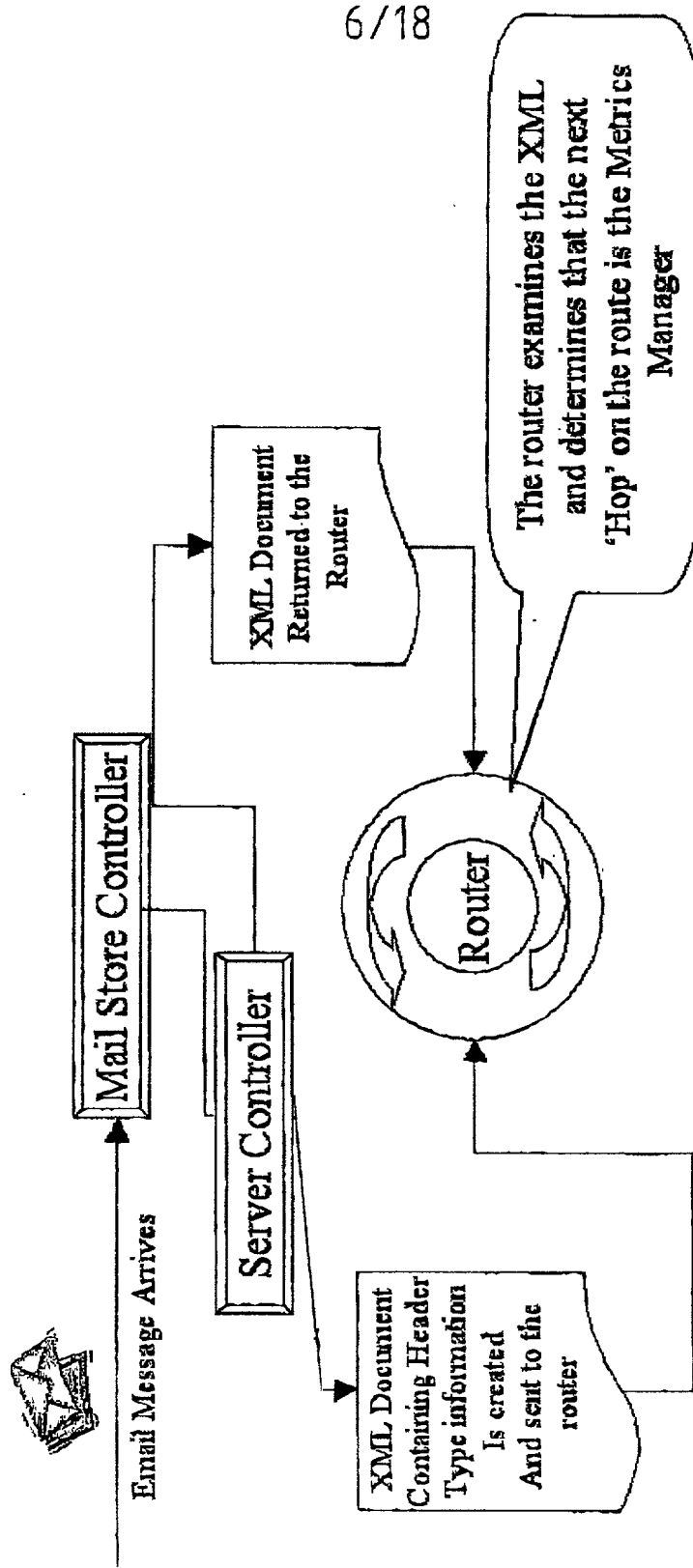


Fig. 6

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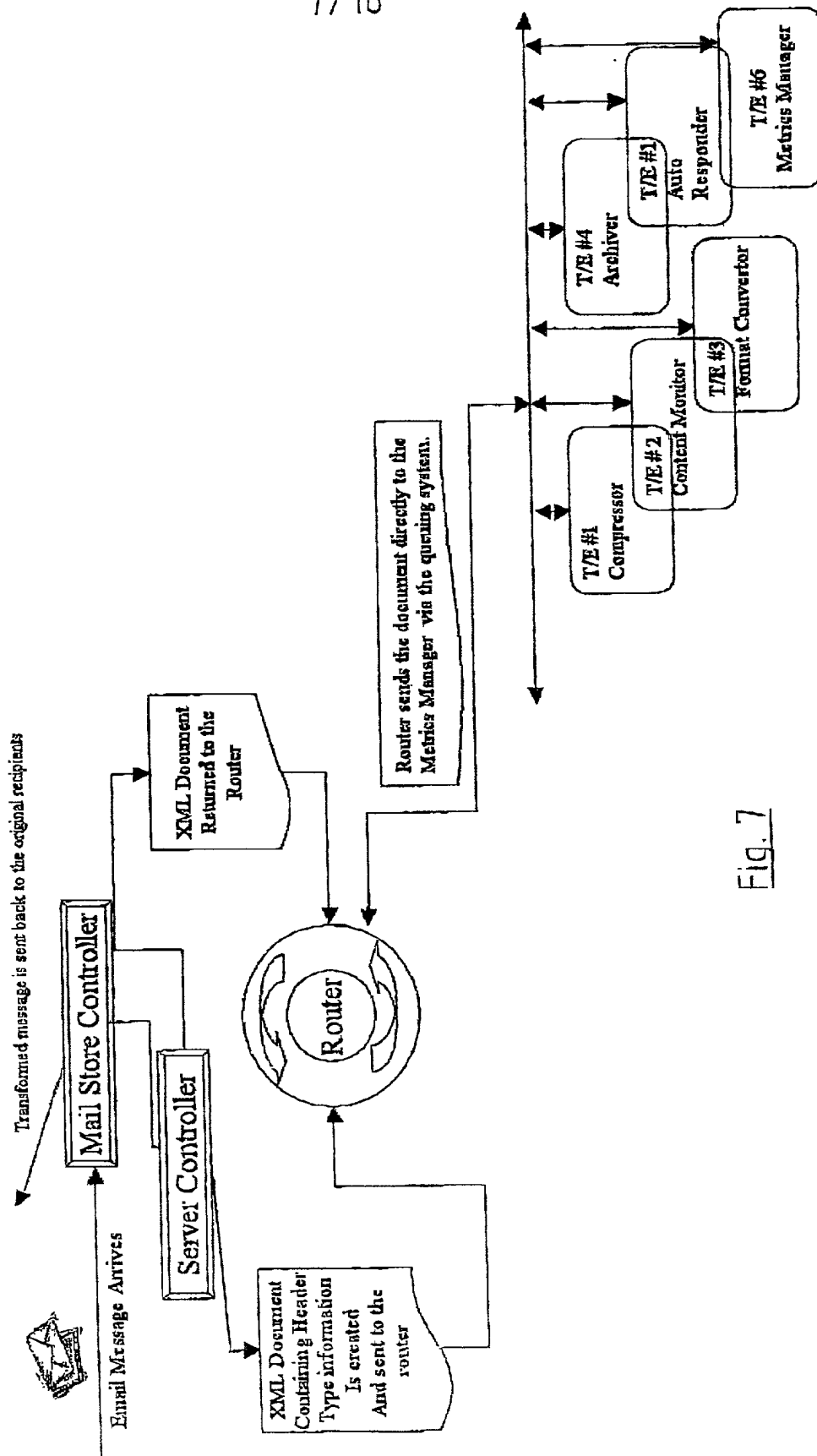


Fig. 7

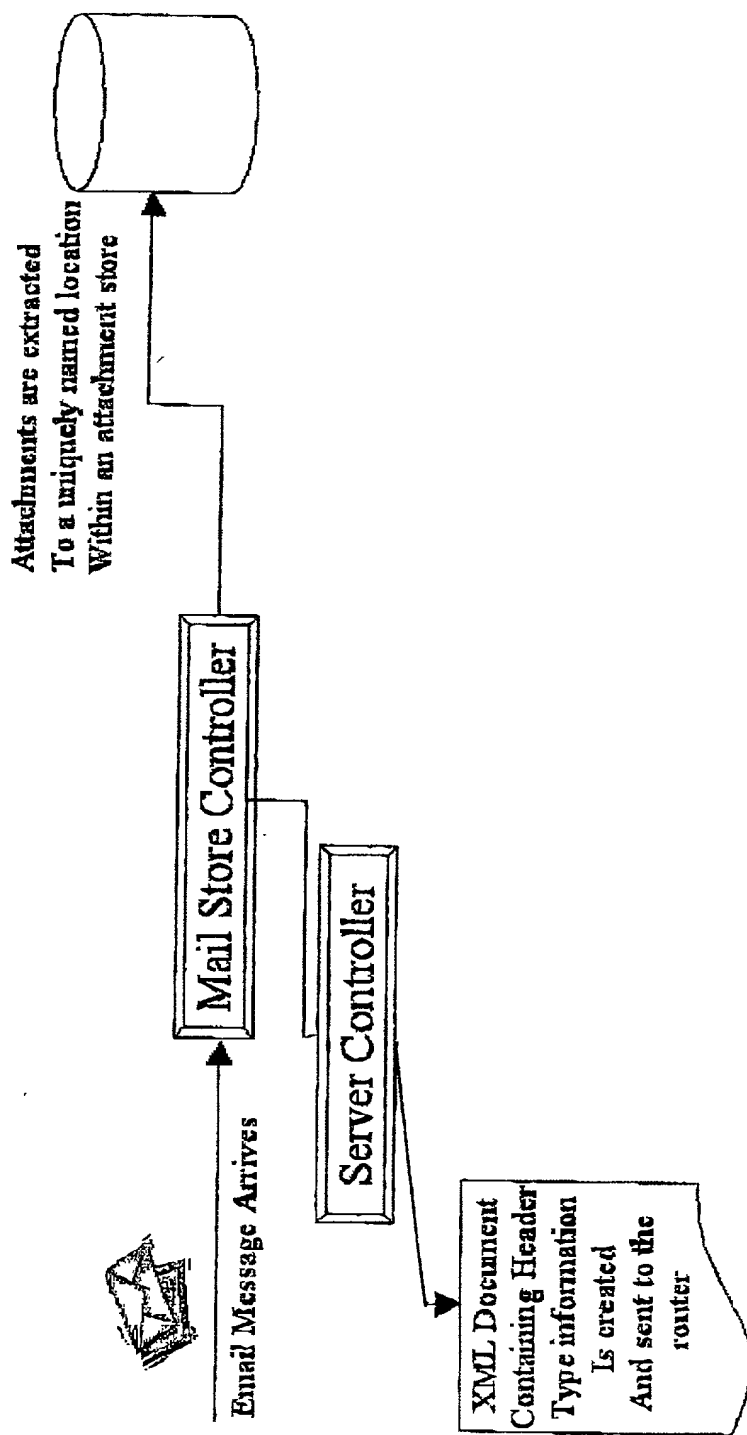


Fig. 8

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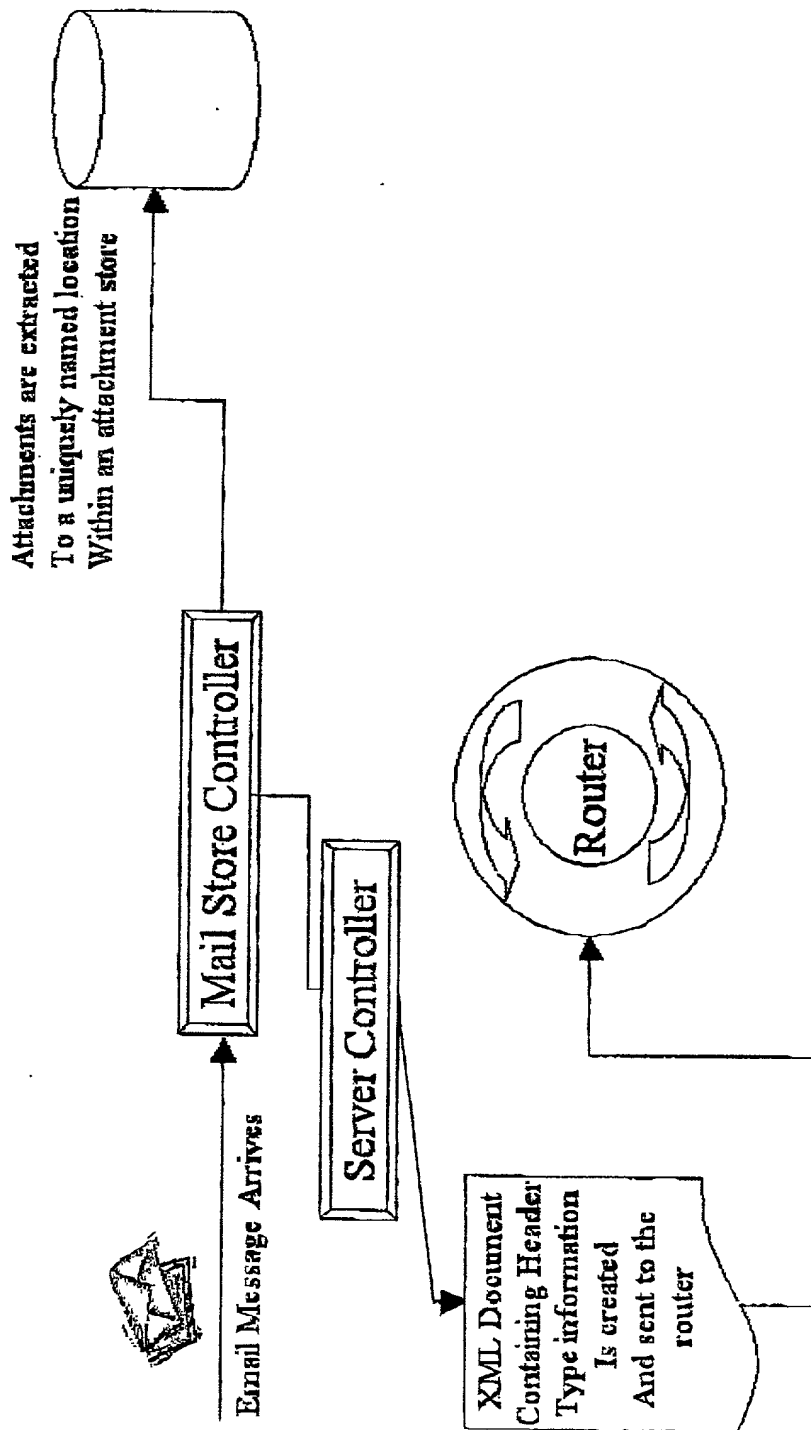


Fig. 9

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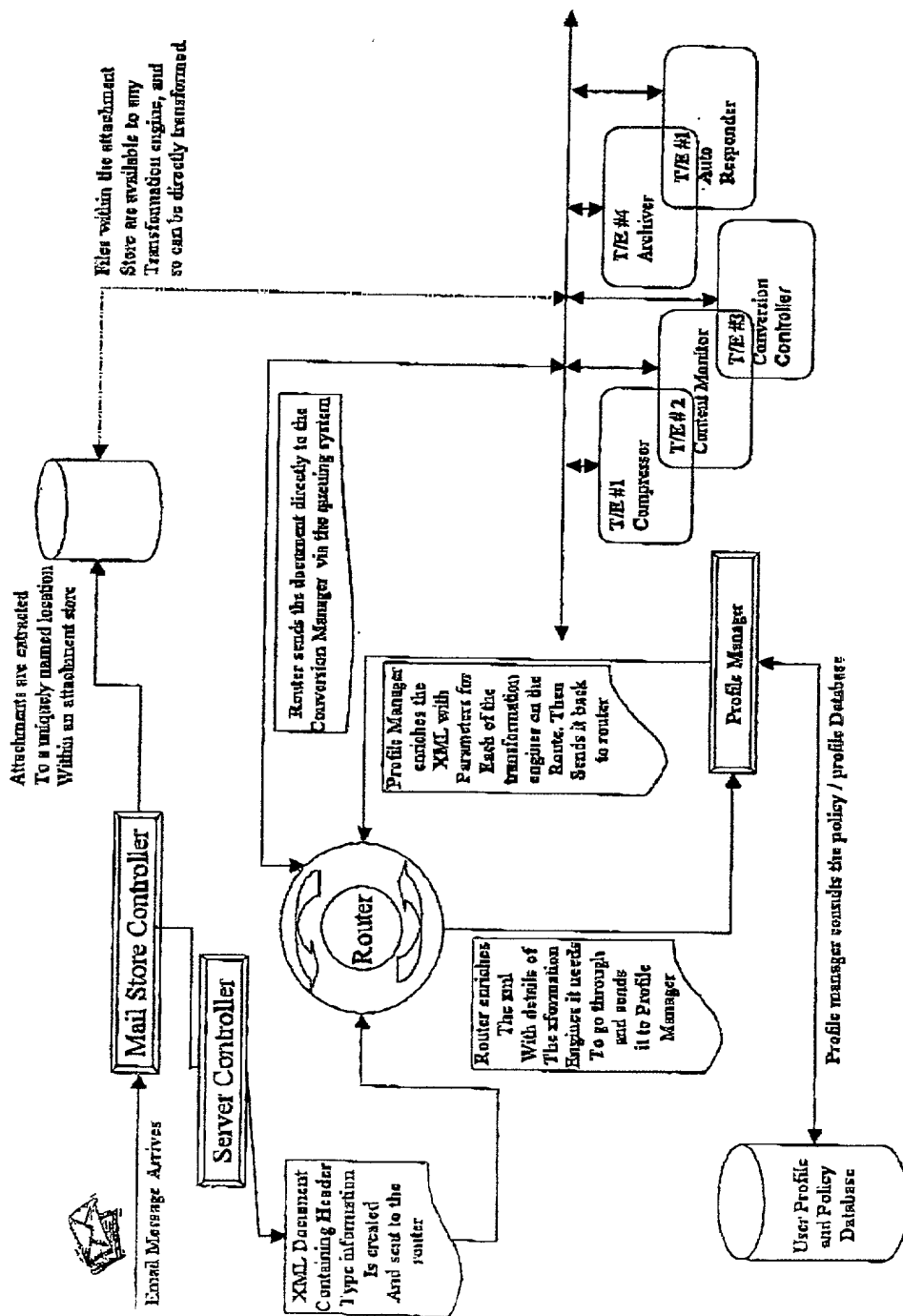


Fig. 10

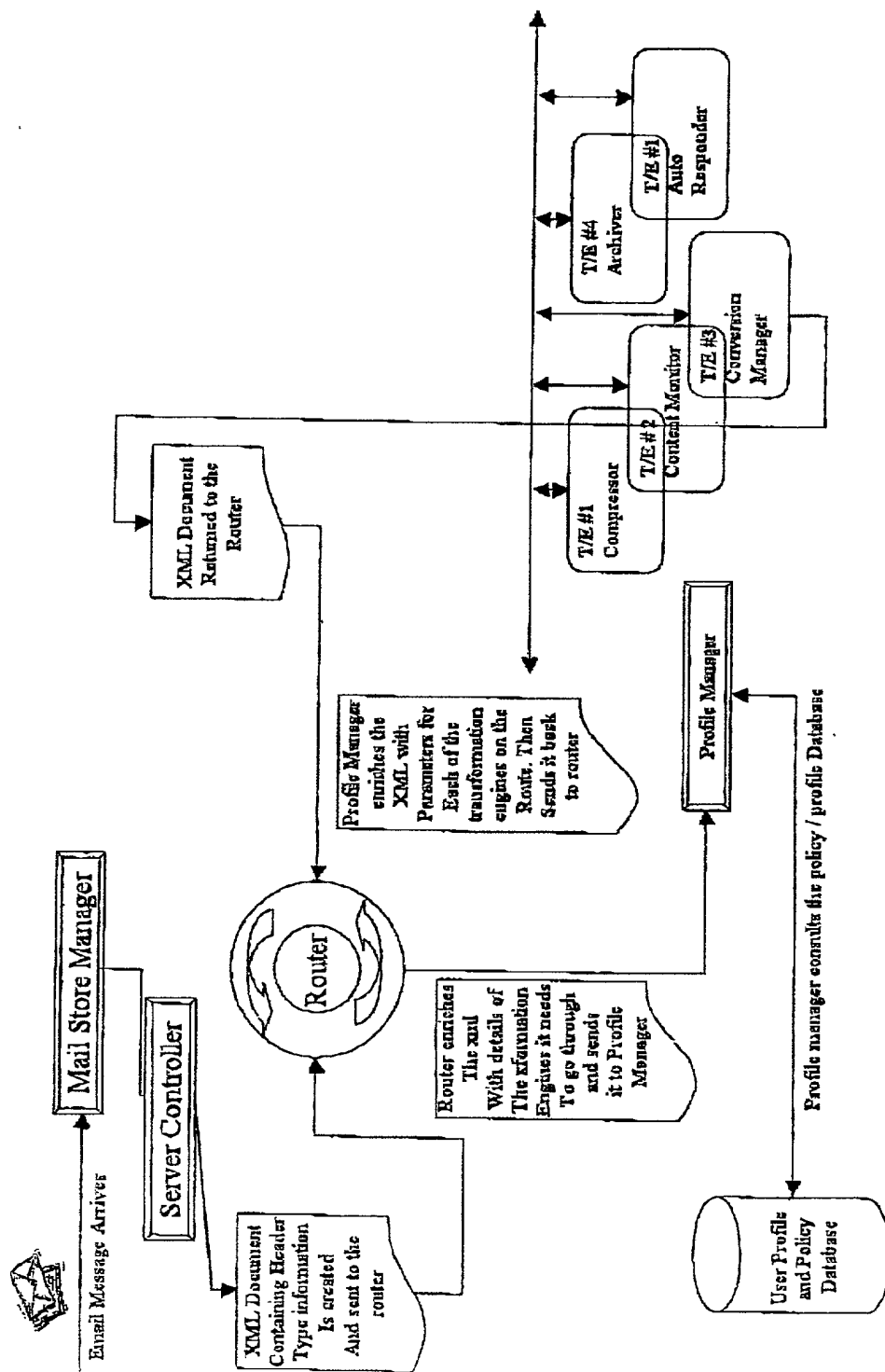
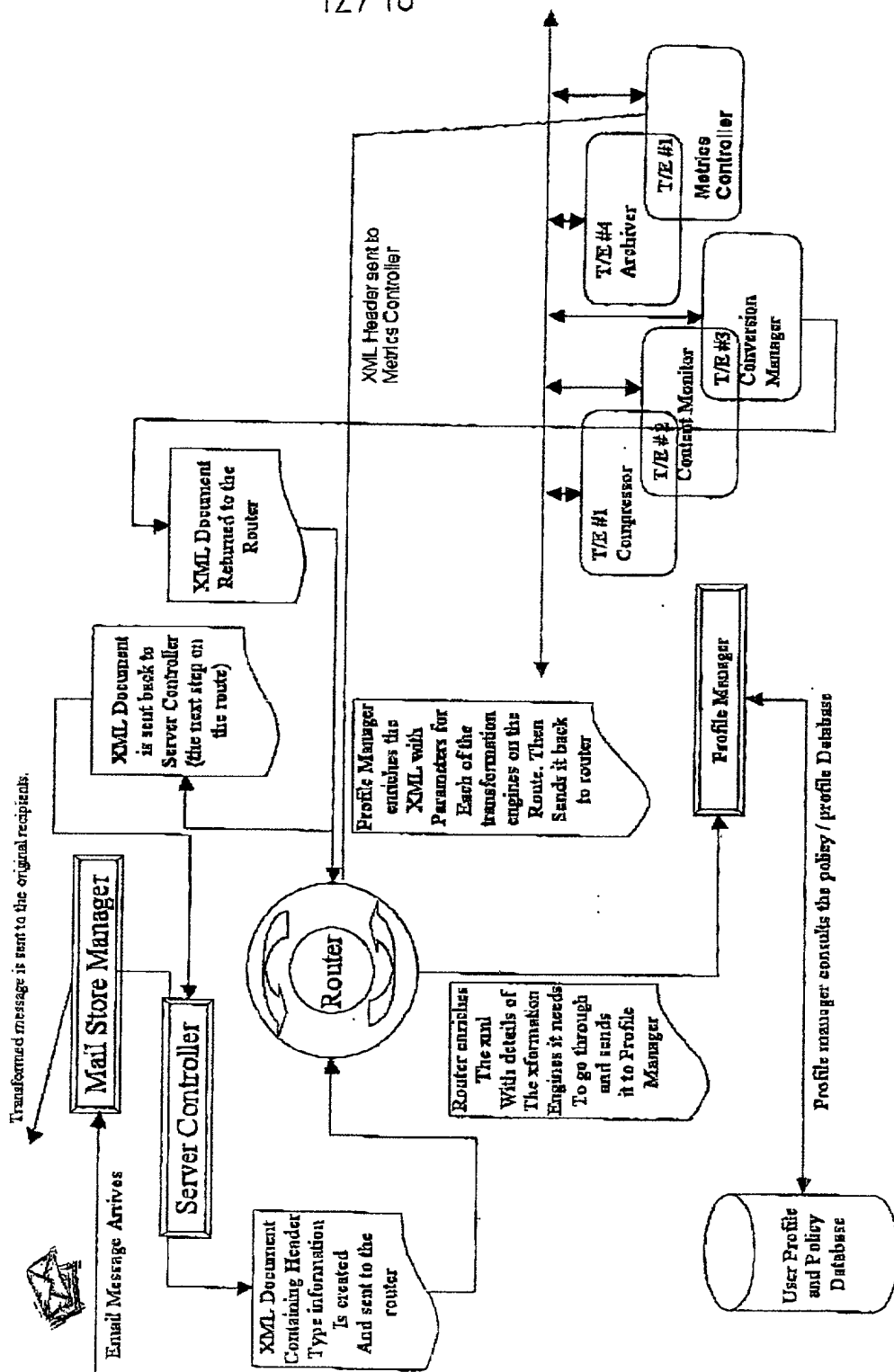


Fig. 11



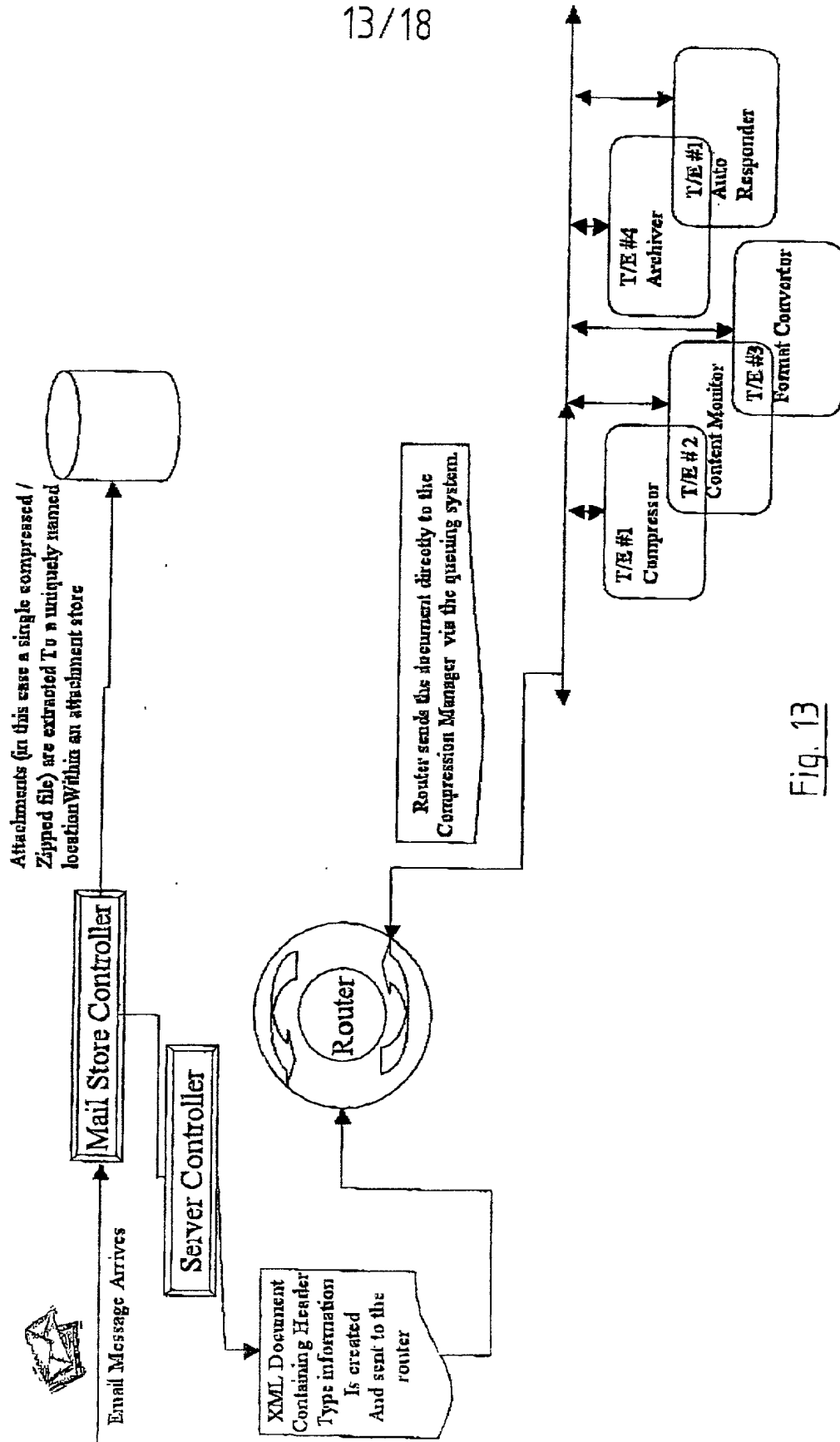


Fig. 13

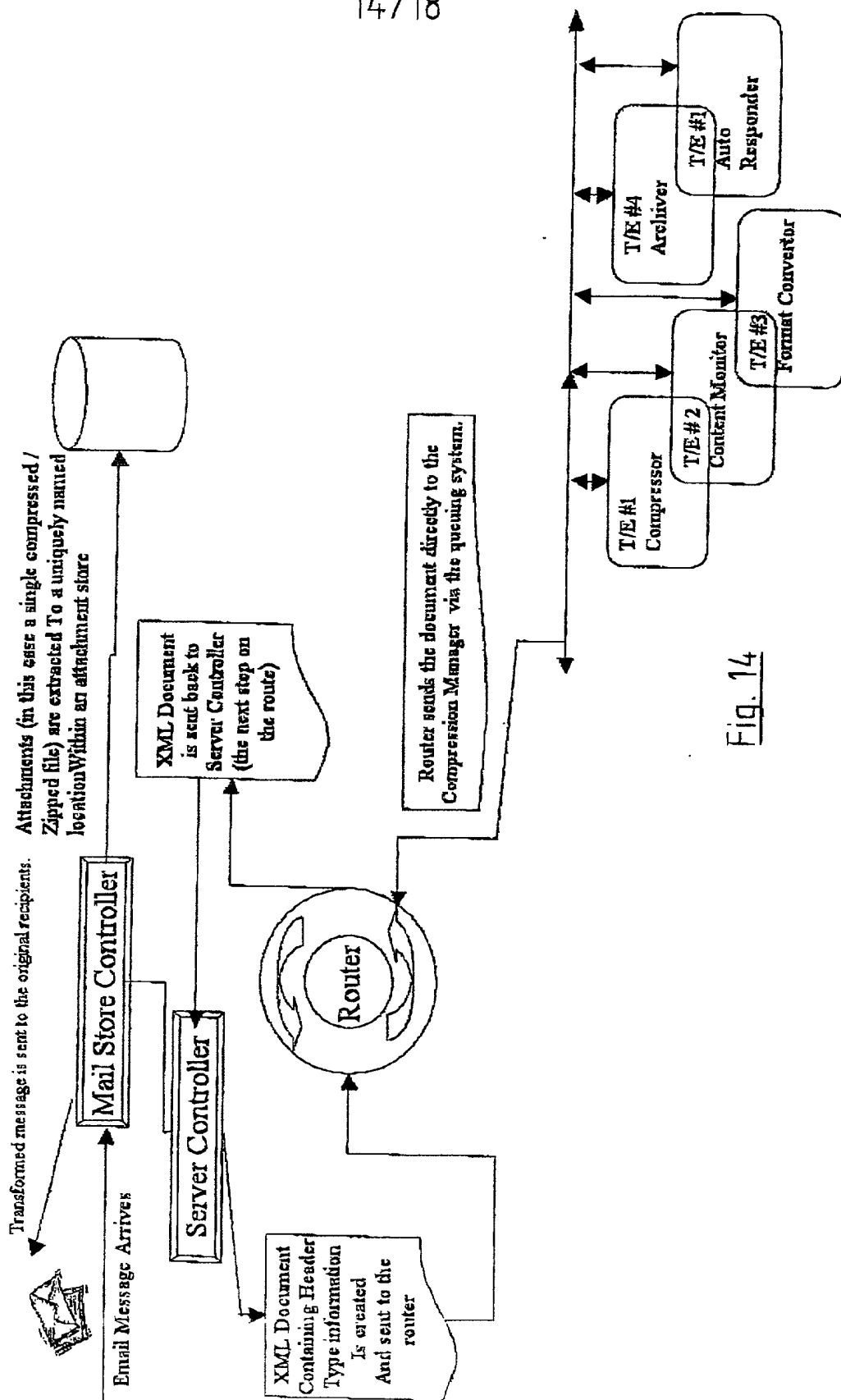


Fig. 14

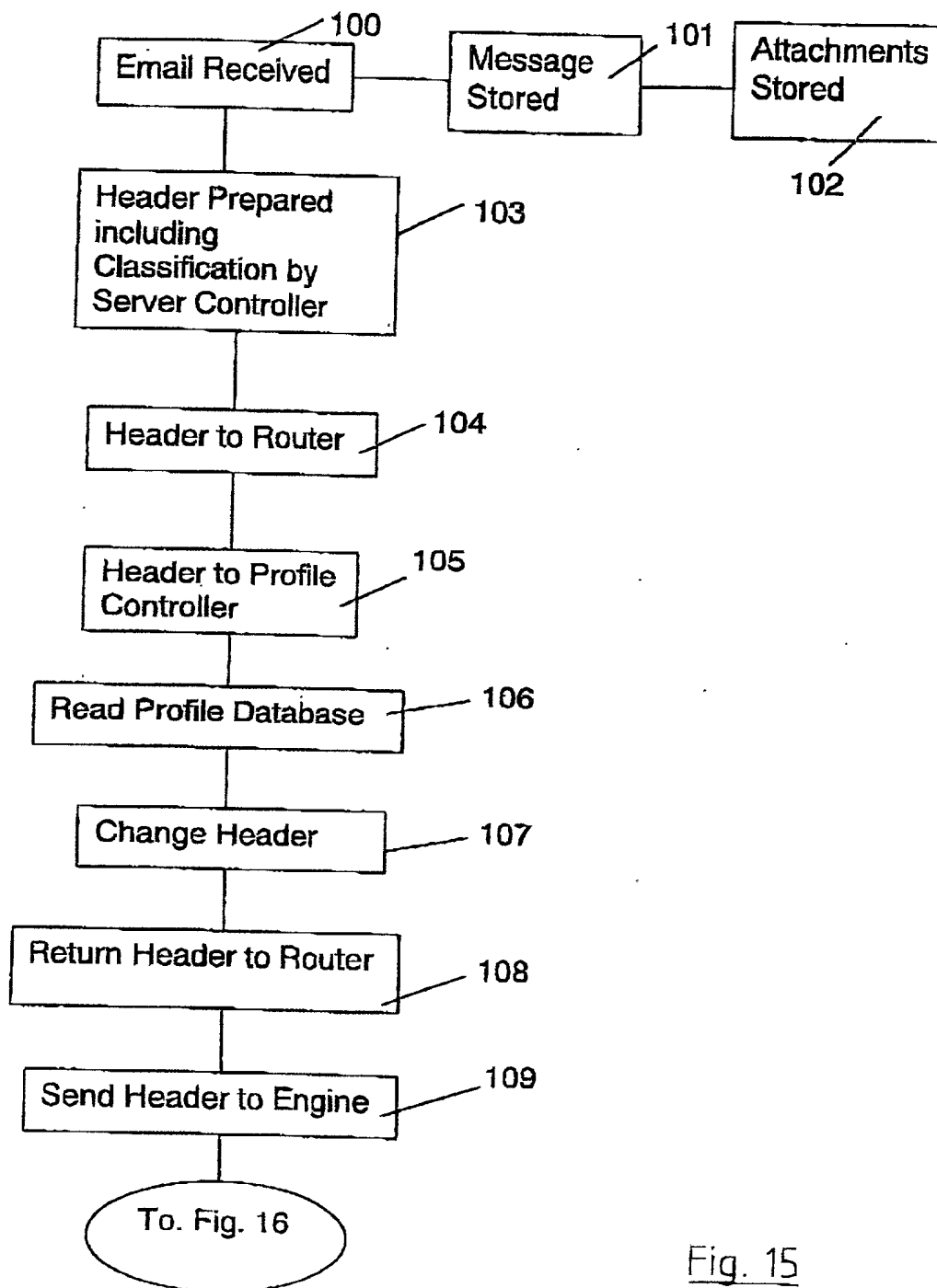


Fig. 15

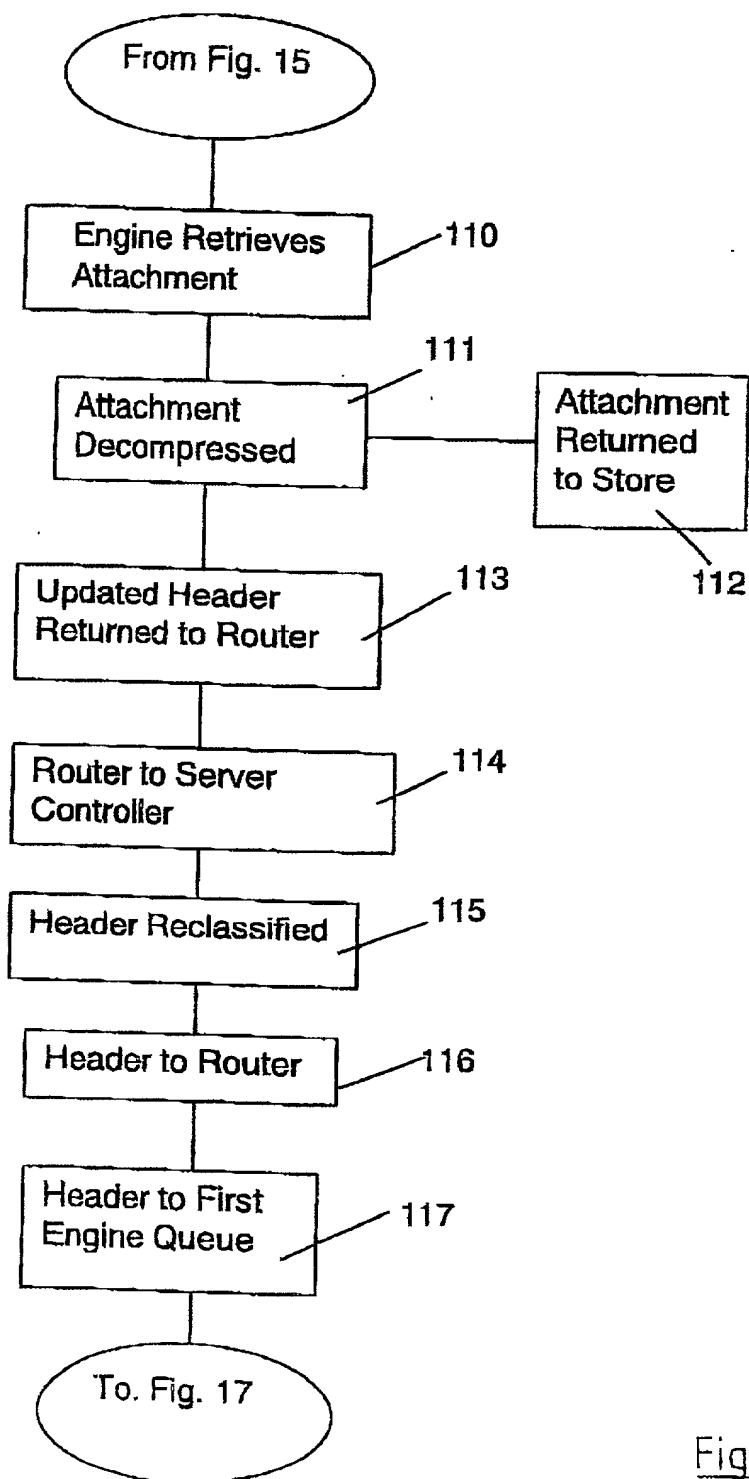


Fig. 16

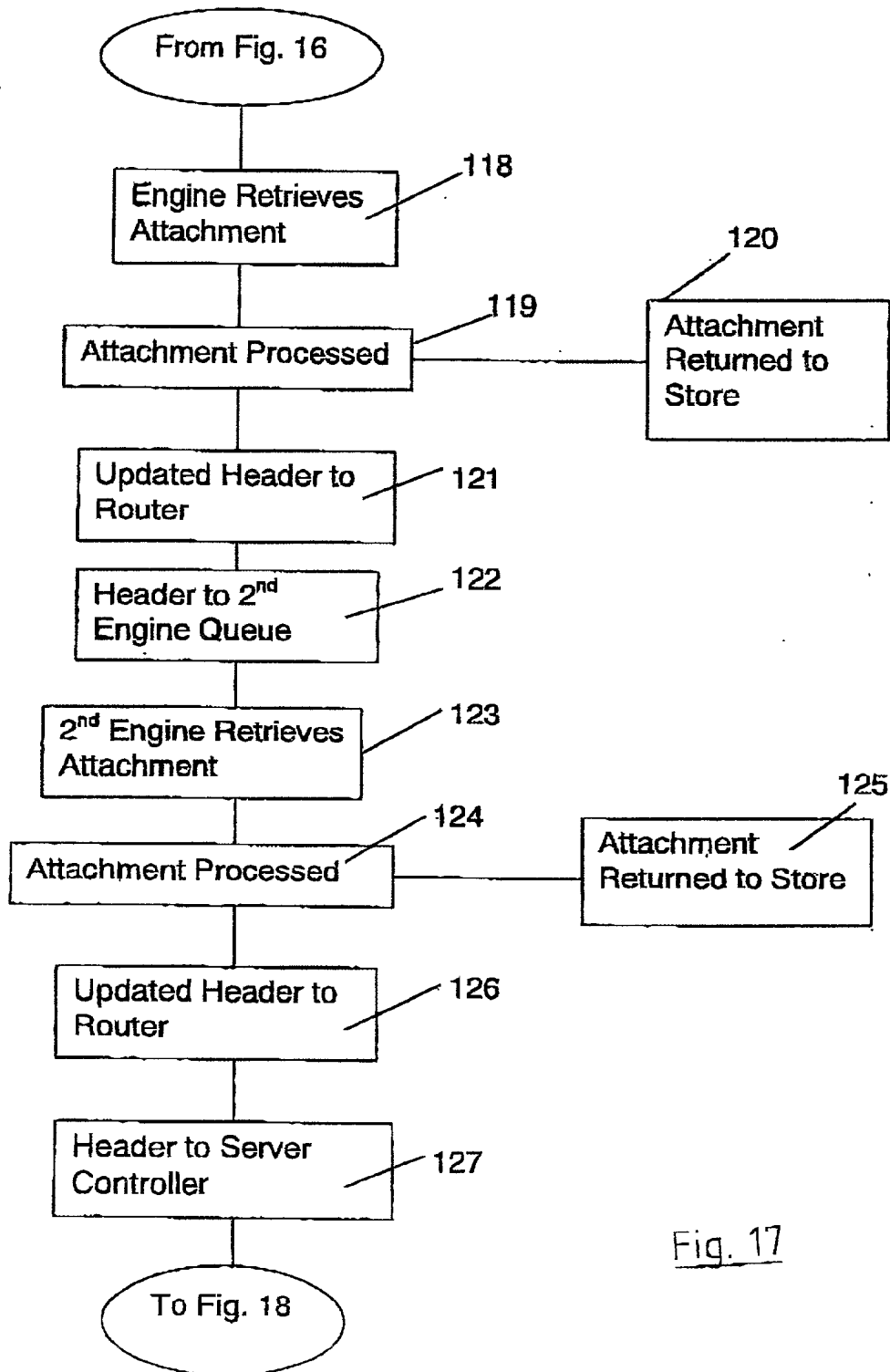


Fig. 17

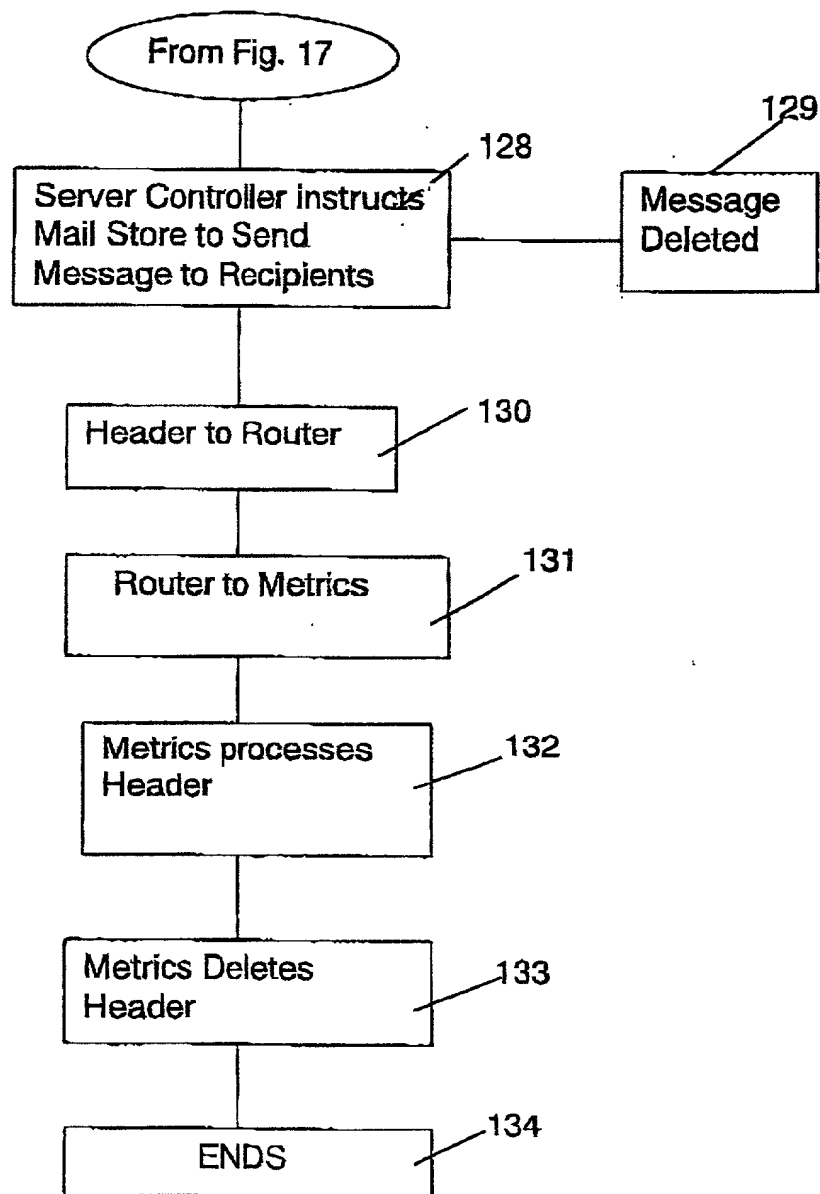


Fig. 18